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JCS21 U.S. PRO
02/22/02PATENT APPLICATION
Attorney's Do. No. 4591-226

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

J10404 U.S. PRO
10/080884
02/22/02

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ADRIENNE CHOCHOLAK
(SENDER'S PRINTED NAME)

Adrienne Chocholak
(SIGNATURE)

Box Patent Application
Assistant Commissioner for Patents
Washington, D.C. 20231

Enclosed for filing is a patent application under 37 CFR 1.53(b) of:

Inventor: Do-Hyung Kim, Sung-Bong Kim and Jung-In Hong
For: METHOD OF FORMING A DEVICE ISOLATION TRENCH IN AN
INTEGRATED CIRCUIT DEVICE

Enclosures:

- ☒ Specification (pages 1-7); claims (pages 8-9); abstract (page 10)
 - ☒ 6 sheet(s) of FORMAL drawings
 - ☒ Declaration or Combined Declaration and Power of Attorney
 - ☒ Newly executed (original or copy)
 - ☐ Copy from a prior application (37 CFR 1.63(d))
 - ☐ Incorporation by Reference--The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.
 - ☐ Deletion of Inventors (signed statement attached deleting inventor(s) named in the prior application (37 CFR 1.63(d)(2) and 1.33(b))
 - ☒ Assignment with cover sheet
- Assignee Name and Address:
- Samsung Electronics Co., Ltd.
416 Maetan-Dong, Paldal-Ku
Suwon-City, Kyungki-Do
Republic of Korea
- ☒ Certified copy of Priority Document No. 2001-0009381, filed February 23, 2001.
 - ☒ Return Postcard

CLAIMS AS FILED

For	Number Filed	Number Extra	Rate	Basic Fee \$740
Total Claims	9-20	0	x \$ 18 =	\$0
Independent Claims	2-3	0	x \$ 84 =	\$0
Multiple Dependent Claim Fee			x \$280 =	
TOTAL FILING FEE				\$740



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PATENT TRADEMARK OFFICE

Respectfully Submitted,

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